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SERIAL NO.: 10/002,405  
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## DRAWINGS

The attached sheets of drawings include changes to Figs. 1-4. Those sheets replace the original sheets of Figs 1-4.

In FIG. 1, "Prior Art" has been added.

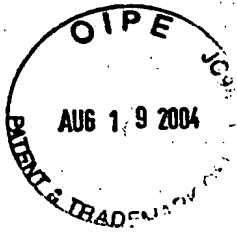
In FIG. 2, in the upper right of the sheet SUB\_BLOCK B has been changed to, and "FORWARD ITERATIVE CALCULATIONS" and "BACKWORD ITERATIVE CALCULATIONS" arrow lines have been added to show calculations directions of SUB\_BLOCK C.

In FIG. 3, reference number 110 has been added to Data Block Parser. Reference numbers 120A, 120B, 120C and 120D have been added to Decoder A, Decoder B, Decoder C and Decoder D, respectively.

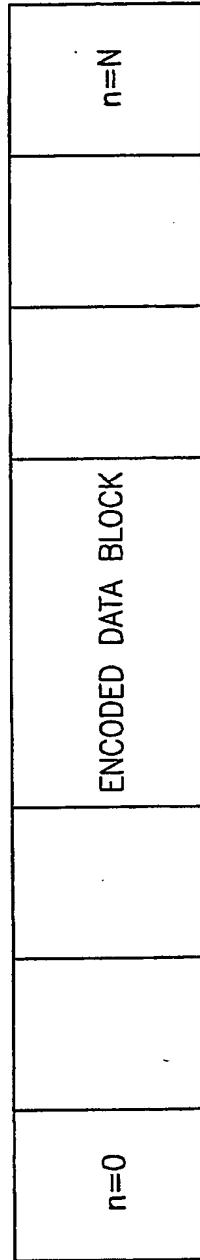
In FIG. 4, a reference number 122B has been added to reference "Backward Calculator" block. A reference number 130 has been added to reference "Temporary Memory" block. A reference number 126B has been added to reference "Output Calculation" block. A reference number 124B has been added to reference "Forward Calculator" block. An arrow has been added to the line from "Backward Calculator" block to "Temporary Memory" block. References  $\beta_n$  have been added to reference WR and RD lines and a reference  $\alpha_n$  has been added to reference the output of block 124A.

Support for the foregoing amendments can be found in the original specification, claims and/or figures. Accordingly, Applicants respectively assert that no new matter has been added.

Attachment: Replacement Sheets



FORWARD ITERATIVE CALCULATIONS



BACKWARD ITERATIVE CALCULATIONS

*FIG. 1*  
PRIOR ART

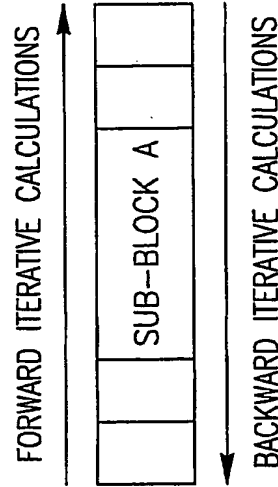
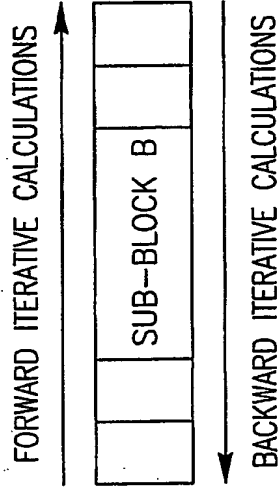
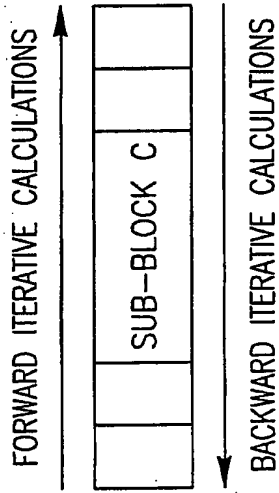


FIG.2

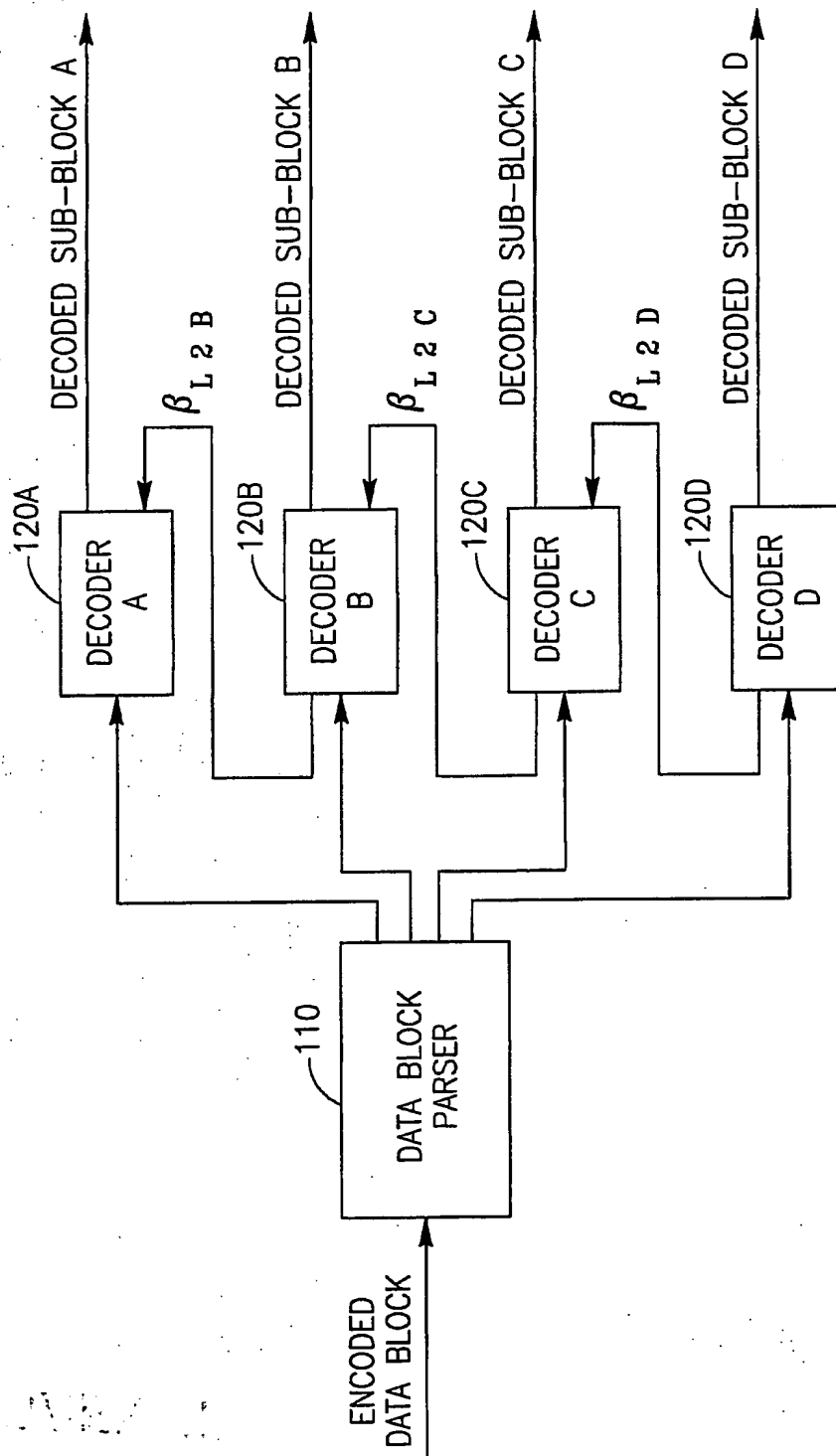


FIG. 3

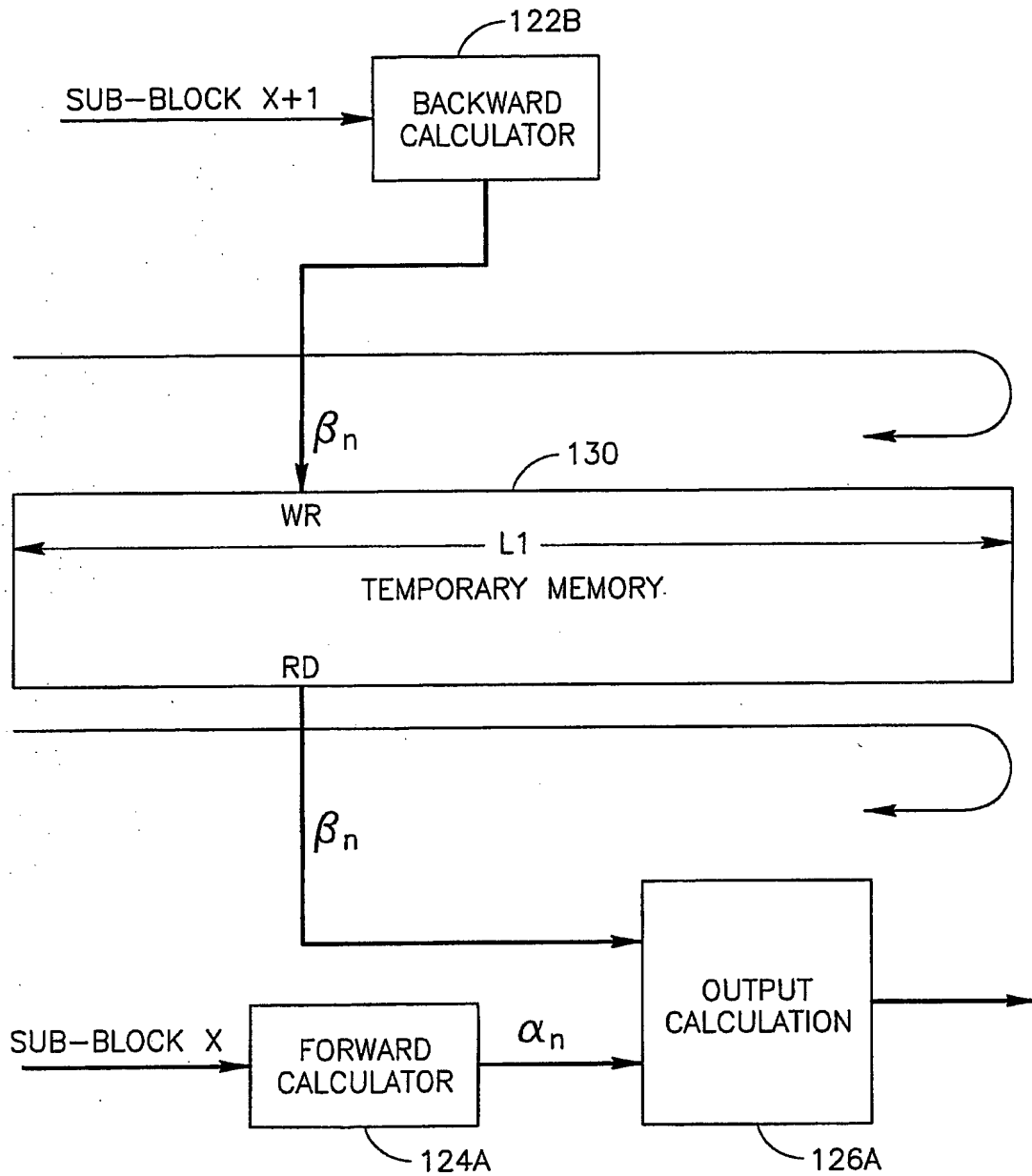


FIG. 4